



SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist.

Department Of Electronics and Communication Engineering



Ref. No: SVEC/ECE/Circular/2024-25/10

CLASS CONSOLIDATED TIME TABLE

Class: V Semester

Section: A

Class Coordinator: Sri G.Shankara Bhaskara Rao

w. e. f. 30-07-2024

Room No.: D-203

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	AWP	MPMC	Mini project	AC	Lunch Break	PCS(E)		AWP
Tue	MPMC LAB					MPMC	VLSI	AC
Wed	PCS(A)		AWP	AC		MPMC	Counseling	VLSI
Thu	VLSI	MPMC	Student Activity	AWP		AC	VLSI	Sports
Fri	MPMC	Mini Project		VLSI		LIB	AWP	AC
Sat	AC	MPMC	VLSI	AWP		VLSI LAB		

Section: B

Class Coordinator: Smt. Y. Sujatha

Room No.: D-202

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	VLSI LAB				Lunch Break	AWP	AC	MPMC
Tue	AC	AWP	VLSI	MPMC		PCS(E)		LIB
Wed	AWP	MPMC	PCS(A)			VLSI	AWP	AC
Thu	MPMC	AWP	AC	VLSI		MPMC LAB		
Fri	VLSI	MPMC	Mini Project			AC	Counseling	VLSI
Sat	VLSI	Student Activity	MPMC	Mini Project		AC	AWP	Sports

Section: C

Class Coordinator: Dr. K. Baboji

Room No.: A-204

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	MPMC	AC	VLSI	MPMC	Lunch Break	Mini Project		AWP
Tue	VLSI LAB					AC	VLSI	MPMC
Wed	VLSI	AC	AWP	Student Activity		MPMC	PCS(A)	
Thu	AC	VLSI	MPMC	AWP		PCS(E)		Sports
Fri	AWP	VLSI	AWP	LIB		MPMC LAB		
Sat	VLSI	Counseling	AWP	AC		MPMC	Mini Project	AC

Staff Details:

S. NO.	SUB	COURSE NAME	SECTION		
			A	B	C
1.	VLSI	VLSI	Smt. Y.SUJATHA	Smt. Y.SUJATHA	Dr. BABOJI
2.	MPMC	Microprocessors & Microcontrollers	Sri M.VINODKUMAR	Sri. RLR. LOKESH BABU	Sri. RLR. LOKESH BABU BABU
3.	AC	Analog Circuits	Dr. B. ASHOK KUMAR	Dr. B. ASHOK KUMAR	Dr. B. ASHOK KUMAR
4.	AWP	Antennas & Wave Propagation	Sri G. SHANKARA BHASKARA RAO	Sri G. SHANKARA BHASKARA RAO	Sri G.V.SUBRAHMANYAM
5.	VLSI lab	VLSI lab	Smt. Y.SUJATHA Dr. M. THAMARAI	Smt. Y.SUJATHA Sri. P. NAGARAJU	Dr. BABOJI Sri P.NAGARAJU
6.	MPMC Lab	Microprocessors & Microcontrollers Lab	Sri M.VINOD KUMAR Dr. KNH. SRINIVAS	Dr. KNH. SRINIVAS Sri M.VINOD KUMAR	Sri G.V.SUBRAHMANYAM Sri K.PASIPALANA RAO
7.	OTJ Lab	OOPs Through Java			
8.	PCS-III	Professional Communication Skills-III	Dr. T. SUJANI Sri T.H.S.SRINIVAS (A)	Sri M.VENKATA RAMANA Sri J.N.V.SOMAYAJULU(A)	Sri.K. V. RAMA RAO Sri T.H.S.SRINIVAS (A)

E. K. Srinivas

Head of the Department

Vision: To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication Engineers.

- Mission:**
- To create a learner centric environment that promotes the intellectual growth of the students.
 - To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.
 - To build the student community with high ethical standards.



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Department Of Electronics and Communication Technology



Ref. No: SVEC/ECT/Circular/2024-25/10

CLASS CONSOLIDATED TIME TABLE

Class: V Semester

w. e. f. 30-07-2024

Section: ECT

Class Coordinator: Sri K. Pasipalana Rao

Room No.: A-203

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	VLSI	AWP	AC	Mini Project	Lunch Break	AC	VLSI	MPMC
Tue	AWP	MPMC	Mini Project			AWP	AC	Sports
Wed	AC	AWP	VLSI	AC		VLSI LAB		
Thu	AC	MPMC	Counseling	VLSI		AWP	PCS(A)	
Fri	MPMC	Student Activity	PCS(E)			MPMC	VLSI	LIB
Sat	MPMC LAB					AWP	MPMC	VLSI

Staff Details:

S. NO.	SUB	COURSE NAME	SECTION
			ECT
1.	VLSI	VLSI	Dr.BABOJI
2.	MPMC	Microprocessors & Microcontrollers	Sri.T.SREENIVASU
3.	AC	Analog Circuits	Sri K.PASIPALANA RAO
4.	AWP	Antennas & Wave Propagation	Sri G.V.SUBRAHMANYAM
5.	VLSI lab	VLSI lab	Dr. BABOJI Sri K.PASIPALANA RAO
6.	MPMC Lab	Microprocessors & Microcontrollers Lab	Sri. T. SREENIVASU Sri K.PASIPALANA RAO
7.	OTJ Lab	OOPs Through Java	
8.	PCS-III	Professional Communication Skills-III	Smt.TANUJA.CH Sri T.H.S.SRINIVAS (A)

E. K. Srinivas

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Department Of Electronics and Communication Engineering



Ref. No: SVEC/ECE/Circular/2024-25/11

CLASS CONSOLIDATED TIME TABLE

Class: VII Semester

w. e. f. 01-08-2024

Section: A

Class Coordinator: **Dr.Ch.V.Naga Bhaskar**

Room No.: A-301

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	CDICD	CMC	CMC	DIP	Lunch Break	MSC	SA	Counseling
Tue	DIP	SA	MSC	OCN		CDICD	SA	Library
Wed	OCN	MSC	DIP	SA		CDCID	DIP	OCN
Thu	MSC	DIP	CDICD	OCN		CMC	SA	CDICD
Fri	SA	CMC	CMC	CDICD		MSC	OCN	DIP

Section: B

Class Coordinator: **Dr.V.Jaya Prakash**

Room No.: A-303

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	SA	MSC	CMC	ML	Lunch Break	CDICD	CMC	ML
Tue	MSC	Counseling	CDICD	DIP		SA	MSC	CDICD
Wed	DIP	CDICD	ML	MSC		CMC	SA	Library
Thu	CDICD	SA	ML	DIP		MSC	CMC	DIP
Fri	CMC	ML	CMC	SA		DIP	ML	MSC

Section: C

Class Coordinator: **Dr.B.Murali Krishna**

Room No.: A-304

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	OCN	DIP	CDICD	SA	Lunch Break	CMC	CDICD	Library
Tue	ML	OCN	CDICD	CMC		SA	DIP	ML
Wed	DIP	ML	SA	CMC		OCN	DIP	CDICD
Thu	SA	OCN	Counseling	CMC		ML	CDICD	OCN
Fri	CDICD	OCN	SA	DIP		ML	SA	CMC

Staff Details:

S. No.	Course Code	Sub	Course Name	SECTION		
				A	B	C
1.	V20ECT20	DIP	Digital Image Processing	Sri. Thota Sreenivas	Sri. Thota Sreenivas	Sri T.Sreenivasu
2.	V20ECT22	CMC	Cellular & Mobile Communication	Dr. E KusumaKumari	Sri. PVV Rajesh	Sri. PVV Rajesh
3.	V20ECT25	CDICD	COMS Digital IC Design	Dr. SVV Satyanarayana	Dr. SVV Satyanarayana	Dr. B Murali Krishna
4.	V20ECTJOC14	ML	Machine Learning		Sri. P Nagaraju	Dr.M.Thamarai
5.	V20ECTJOC02(Elective)	OCN	Optical Communication Networks	Dr. Ch. V. Nagabhaskar	--	Dr. Ch. V. Nagabhaskar
6.	V20ECTJOC04	MSC	Modern Satellite Communication	Dr. V.Jaya Prakash	Dr. V.Jaya Prakash	
7.	V20ECTJOC12(Elective)	SA	Sensors & Applications	Sri. V Anil Kumar	Sri. V Anil Kumar	Dr.K.N.H.Srinivas

E. KusumaKumari

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Department Of Electronics and Communication Technology



Ref. No: SVEC/ECT/Circular/2024-25/11

CLASS CONSOLIDATED TIME TABLE

Class: VII Semester

w. e. f. 01-08-2024

Section: ECT

Class Coordinator: Sri P. Nagaraju

Room No.: Seminar Hall

Periods	1	2	3	4		5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:00-2:00	2:00-2:50	2:50-3:40	3:40-4:30
Mon	CDICD	SA	Library	MSC	Lunch Break	DIP	MSC	SA
Tue	CDICD	Counseling	ML	DIP		CMC	CDICD	MSC
Wed	CMC	MSC	DIP	ML		SA	ML	CMC
Thu	ML	CDICD	SA	DIP		CMC	DIP	ML
Fri	SA	ML	MSC	CMC		CDICD	MSC	DIP

Staff Details:

S. No.	Course Code	Sub	Course Name	ECT
1.	V20ECT20	DIP	Digital Image Processing	Sri T.Sreenivasu
2.	V20ECT22	CMC	Cellular & Mobile Communication	Sri. P Nagaraju
3.	V20ECT25	CDICD	COMS Digital IC Design	Dr. B Murali Krishna
4.	V20ECTJOC14	ML	Machine Learning	Dr.M.Thamarai
5.	V20ECTJOC04	MSC	Modern Satellite Communication	Sri M. Vinod Kumar
6.	V20ECTJOC12	SA	Sensors & Applications	Dr.K.N.H.Srinivas

E. K. Srinivas

Head of the Department

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Department of Electronics and Communication Engineering



Ref. No: SVEC/ECE/Circular/2024-25/22

CLASS CONSOLIDATED TIME TABLE

Class: III Semester

Section: A

Class Coordinator: Smt. J. Dhanasree

w.e.f.10-09-2024

Room No.: D-302

Periods	1	2	3	4	1:00-2:00	5	6	7	
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30	
Mon	PTSP	STLD	EDC	ES	Lunch Break	Data Structures Lab			
Tue	Data Structures Lab					STLD LAB			
Wed	SS	PTSP	PCS			Data Structures Lab			
Thu	Data Structures Lab					UHV	EDC	STLD	
Fri	SS	EDC LAB				Data Structures Lab			
Sat	Data Structures Lab					ES	SS	PTSP	

Section: B

Class Coordinator: Sri M. Pitchaiah

Room No.: D-303

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	Data Structures Lab				Lunch Break	PCS-I		SS
Tue	STLD	PTSP	EDC	UHV		Data Structures Lab		
Wed	Data Structures Lab					UHV	STLD	ES
Thu	SS	UHV	STLD	PTSP		Data Structures Lab		
Fri	Data Structures Lab					PTSP	SS	EDC
Sat	PTSP	STLD	EDC	SS		Data Structures Lab		

Section: C

Class Coordinator: Sri P. Gopala Reddy

Room No.: D-304

Periods	1	2	3	4	1:00-2:00	5	6	7	
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30	
Mon	EDC	PTSP	STLD	SS	Lunch Break	Data Structures Lab			
Tue	Data Structures Lab					EDC	ES	PTSP	
Wed	SS	STLD LAB				Data Structures Lab			
Thu	Data Structures Lab					PTSP	UHV	STLD	
Fri	STLD	ES	SS	UHV		Data Structures Lab			
Sat	Data Structures Lab					PCS-I		EDC	

Staff Details:

S. No.	Sub	COURSENAME	SECTION		
			A	B	C
1.	EDC	Electronic Devices And Circuits	Sri.D.Venkanna Babu	Sri.D.Venkanna Babu	Sri R.Ram Prasad
2.	SS	Signals & Systems	Sri D.R.Sandeep	Sri D.R.Sandeep	Dr.T.V.N.L.Aswini
3.	PTSP	Probability Theory & Stochastic Process	Smt.J.Dhanasree	Sri J.Rajendra	Sri J.Rajendra
4.	STLD	Switching Theory And Logic Design	Sri M.Pitchaiah	Sri M.Pitchaiah	Sri P.Gopala Reddy
5.	UHV	Universal Human Values- Understanding Harmony And Ethical Human Conduct	Dr.M.Thamarai	Dr.E.Kusuma Kumari	Sri R.L.R.Lokesh Babu
6.	ES	Environmental Science	Smt. P Durga Devi	Ms. S Lavanya	Ms.Sd. Yasmin
7.	EDC LAB	Electronic Devices And Circuits Lab	Sri.D.Venkanna Babu Dr. B. Ashok Kumar Sri R.Ram Prasad Sri. P. Srinivas	Sri Thota Srieenivas Dr. Ch.V. Naga Bhaskar Dr.T.V.N.L.Aswini Sri. P. Srinivas	Sri R.Ram Prasad Dr. Ch.V. Naga Bhaskar Sri Thota Srieenivas Sri J.Rajendra
8.	STLD LAB	Switching Theory And Logic Design Lab	Sri M.Pitchaiah Sri PVV. Rajesh Sri D.R.Sandeep Sri. G. Shnakara Bhaskara Rao	Dr.V.Jaya Prakash Sri M.Pitchaiah Sri D.R.Sandeep Sri. G. Shnakara Bhaskara Rao	Sri P.Gopala Reddy Sri M.Pitchaiah Dr. T.V.N.L.Aswini Sri P.Srinivas
9.	PCS-II	Professional Communication Skills-I	Smt. K. Radha Madhavi Sri K. V. Rama Rao	Smt.Tanuja Ch Sri M. Venkata Ramana	Dr. T.Sujani Sri M. Venkata Ramana

E. Kusuma Kumari
Head of the Department

Vision

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Mission

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Department of Electronics and Communication Technology



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CLASS CONSOLIDATED TIME TABLE

Class: III Semester

Section: ECT

Class Coordinator: Sri P. Srinivas

w.e.f.10-09-2024

Room No.: D-305

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	Data Structures Lab				Lunch Break	PTSP	SS	EDC
Tue	PTSP	EDC	SS	STLD		Data Structures Lab		
Wed	Data Structures Lab					PTSP	SS	STLD
Thu	STLD	EDC	PTSP	SS		Data Structures Lab		
Fri	Data Structures Lab					PCS-I		UHV
Sat	EDC	STLD LAB				Data Structures Lab		

Staff Details:

S. NO.	SUB	COURSE NAME	SECTION
			ECT
1.	EDC	Electronic Devices And Circuits	Sri R.Ram Prasad
2.	SS	Signals & Systems	Dr.T.V.N.L.Aswini
3.	PTSP	Probability Theory & Stochastic Process	Sri P.Srinivas
4.	STLD	Switching Theory And Logic Design	Sri P.Gopala Reddy
5.	UHV	Universal Human Values– Understanding Harmony And Ethical Human Conduct	Sri R.L.R.Lokesh Babu
6.	ES	Environmental Science	Sri Jalem N B V Chandra Sekhar
7.	EDC LAB	Electronic Devices And Circuits Lab	Sri R.Ram Prasad Dr. T.V.N.L. Aswini Sri Thota Srieenivas Sri J.Rajendra
8.	STLD LAB	Switching Theory And Logic Design Lab	Sri. G. Shnakara Bhaskara Rao Sri P.Srinivas Dr. V. Jaya Prakash Dr. Ch.V. Naga Bhaskar
9.	PCS-II	Professional Communication Skills-I	Sri K.V.Rama Rao Smt. P.V.Padmavathi

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